WHAT IS CLAIMED IS:

1. A method for automatically developing a testing program of a tester, comprising steps of:

establishing an intellectual property comprising a tester library, a tester resource installation configuration and a testing strategy;

integrating said intellectual property with a product target specification, an error code list and a program transfer rule check; and

automatically developing a source code prototype of said testing program.

2. The method according to claim 1, wherein said tester is one of a digital tester and an analog tester.

3. The method according to claim 1, wherein said tester library comprises pattern file formats and source code prototypes for a plurality of known testers.

4. The method according to claim 1, wherein said tester resource installation configuration comprises Pin electronics (PE) specification and maximum channels, a precision measurement unit (PMU) specifications, a device power supplies (DPS) specification, a time measurement unit (TMU) specification, a vector memory size specification, a system clock rate specification and an analog channel specification.

5. The method according to claim 1, wherein said testing strategy comprises a testing item selected from one of a logical product and an analog product.

6. The method according to claim 5, wherein said testing item of said logical product is one selected from a group consisting of continuity test, drive/sink current test, power dissipation test, IDDQ test, input leakage

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current test, function pattern test and AC characteristic test.

7. The method according to claim 5, wherein said testing item of said analog product is one selected from a group consisting of ADC/DAC's SNR test, THD test, Jitter/Skew test, crosstalk test, eye diagram test and frequency response test.

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